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Docket: 0756-1131

Date: October 28, 1994



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

Transmitted herewith for filing is Continuation-in-Part Patent Application
Based on U.S. Serial No. 08/160,909 filed December 3, 1993:

Inventors: Shunpei YAMAZAKI, Hiroyuki SHIMADA,
Akira TAKENOUCI, Yasuhiko TAKEMURA

For: METHOD FOR PROCESSING SEMICONDUCTOR DEVICE,
APPARATUS FOR PROCESSING A SEMICONDUCTOR AND
APPARATUS FOR PROCESSING SEMICONDUCTOR DEVICE

Enclosed are:

- ☒ [X] 37 pages of specification and claims
- ☒ [X] 10 sheets of formal drawings (Figures 1-11)
- ☒ [X] An assignment of the invention to Semiconductor Energy Laboratory Co., Ltd.
- ☒ [X] Declaration and Power of Attorney.
- ☒ [X] Priority is claimed based on foreign application(s)
No(s): 5-301172 and 6-100642
Filed on: November 5, 1993 and April 13, 1994
Filed in: Japan
Certified copy(s) ☒ [X] attached
- ☐ [] A verified statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.27.

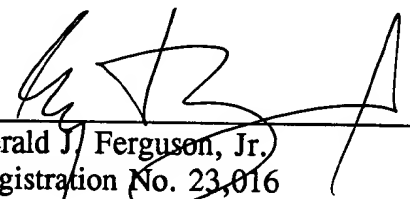
[X] The filing fee has been calculated as shown below.

For:	No. Filed		No. Extra	Rate Sm/Lg Entity	Fee
Basic Fee				\$365/730	\$730.00
Total Claims	40	- 20	20	x 11/22	\$440.00
Independent Claims	12	- 3	9	x 38/76	\$684.00
First presentation of multiple dependent claims \$120/240					
Assignment Recordation Fee					\$40.00
Total					\$1,894.00

[X] A check in the amount of \$1,894.00 to cover the filing fee is enclosed.

[X] The Commissioner is hereby authorized to charge fees under 37 CFR 1.16, 1.17, 1.20(a), 1.20(b), 1.20(c) and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment, to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,



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